CERTIFICATE OF Applicant(s): Agnello	Docket No. FIS920030026US1			
Application No. 10/604,912	Filing Date August 26, 2003	Examiner Unknown	Customer No. 29154	Group Art Unit 2812
Invented: FMETHOD TO PRODUCE TRANSISTOR HAVING REDUCED GATE HEIGHT				
PRADEMARKO				
I hereby certify that this SUBMISSION OF FORMAL DRAWINGS (Identify type of correspondence)				
is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 6/22/04 (Date)				
Frederick W. Gibb, III (Typed or Printed Name of Person Majling Correspondence) Thumbour the second secon				
(Signature of Person Mailing Correspondence)				
	Note: Each pa	per must have its own certificate of mailing.		

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Park et al.

Serial No.: 10/604,912

Group Art: 2812

Filing Date: August 26, 2003

Examiner: Unknown

For:

METHOD TO PRODUCE TRANSISTOR HAVING REDUCED GATE HEIGHT

Commissioner of Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Sir:

Submitted herewith are five (5) sheets of formal drawings comprising Figs. 1A-15B for the above-referenced patent application. Acknowledgment of receipt is respectfully requested. Please substitute these formal drawings for the drawings which were filed with the application.

Respectfully Submitted,

Frederick W. Gibb, III

Reg. No. 37,629

McGinn & Gibb, PLLC

2568-A Riva Road, Suite 304

Annapolis, Maryland 21401

(410) 573-1545

Customer No. 29154